TOSHIBA Diodes for Protecting against ESD

DF2B5M4SL

Application

- ESD Protection

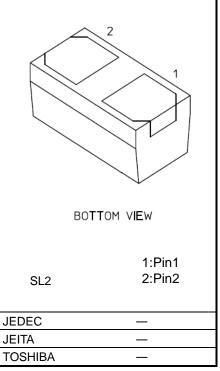
Note: This product is designed for protection against electrostatic discharge (ESD) and is not intended for any other purpose, including, but not limited to, voltage regulation.

Abusolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Electrostatic discharge voltage IEC61000-4-2(Contact) IEC61000-4-2(Air)	V _{ESD} (Note 1)	± 23 ± 25	kV
Peak pulse power (tp = 8 / 20 s)	P _{PK}	30	W
Maximum peak pulse current (tp = 8 / 20 s)	I _{PP} (Note 2)	2	Α
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	−55 to 150	°C

Note1: according to IEC61000-4-2 Note2: according to IEC61000-4-5

Note3:Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/ "Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

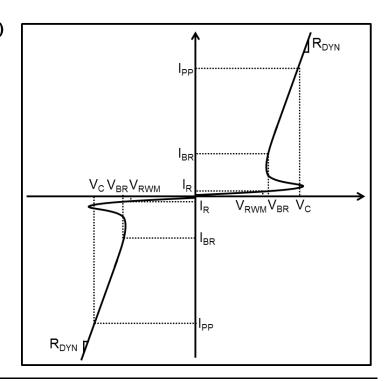


Weight: 0.2 mg (typ.)

Electrical Characteristics (Ta = 25°C)

 V_{RWM} : Working peak reverse voltage V_{BR} : Reverse breakdown voltage I_{BR} : Reverse breakdown current

$$\begin{split} &I_R: Reverse \ Current \\ &V_C: Clamping \ Voltage \\ &I_{PP}: Peak \ pulse \ current \\ &R_{DYN}: Dynamic \ resistance \end{split}$$



Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse stand-off voltage	V _{RWM}	_	_	_	3.6	V
Reverse breakdown voltage	V_{BR}	I _{BR} = 1 mA	4.0	5.0	6.0	V
Reverse current	I _R	V _{RWM} = 5.5 V	_	_	0.1	μА
Clamping Voltage	Vc	I _{PP} =1A (Note1)	_	7.5	_	V
	Vc	I _{PP} =2A (Note1)	_	10	15	٧
Clamping Voltage	Vc	ITLP=16A (Note2)	_	17	_	V
	Vc	ITLP=30A (Note2)	_	24	_	V
Dynamic resistance	R _{DYN}	(Note2)	_	0.5	_	Ω
Total capacitance	C _t	V _R = 0 V, f = 1 MHz (Note3)	_	0.2	0.3	pF

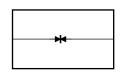
Note1 : Based on IEC61000-4-5 8/20 μ s pulse.

Note2 : TLP parameter: Z0 = 50 $\,\Omega$, tp = 100ns, tr = 300ps, averaging window: t1 = 30 ns to t2 = 60 ns,

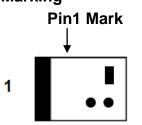
extraction of dynamic resistance using least squares fit of TLP characteristics between IPP1 = 8A and IPP2 = 16A.

Note3: Guaranteed by design.

Equivalent Circuit (Top View)

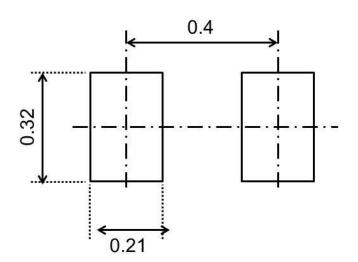


Marking

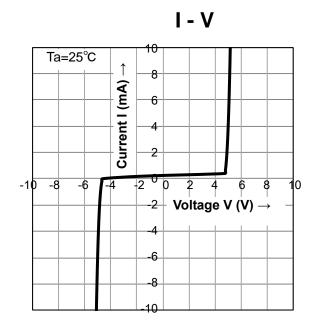


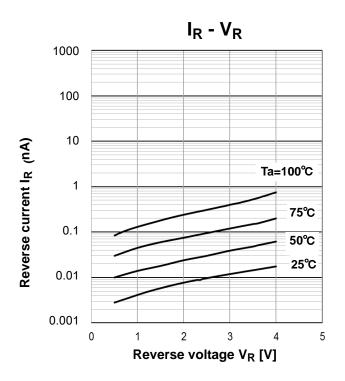
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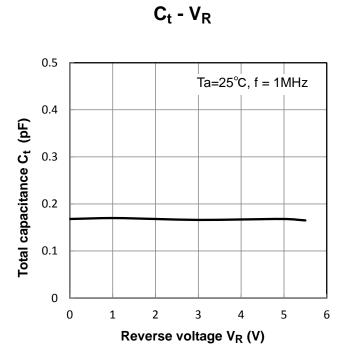
Land Pattern Dimensions for Reference Only (Unit: mm)

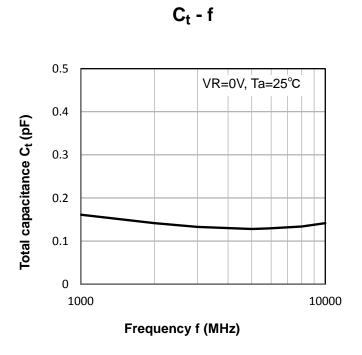


Characteristics Curves (Note)

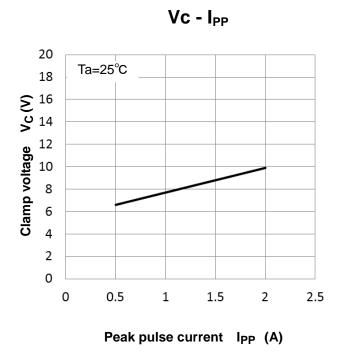








Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Based on IEC61000-4-5 8/20 μ s pulse.(Ed2)

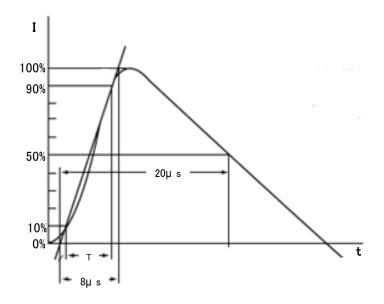
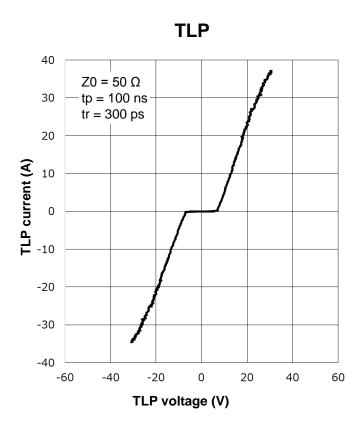


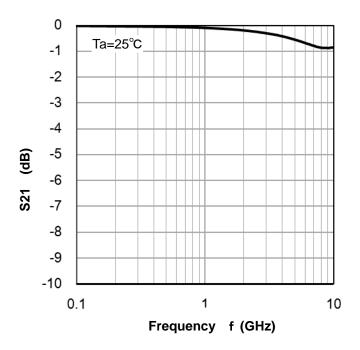
Fig Based on IEC61000-4-5 8/20 µs pulse.(Ed.2)



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Insertion Loss (S21)

S21-f



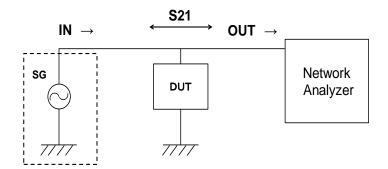


Fig. S21 measurement circuit

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ESD Clamp Waveform (IEC61000-4-2) (Note)

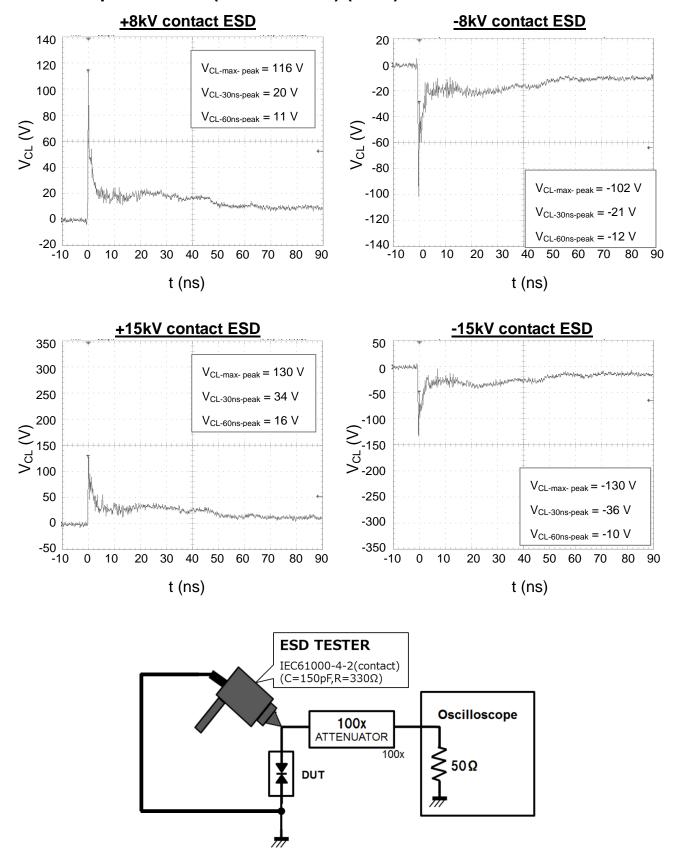
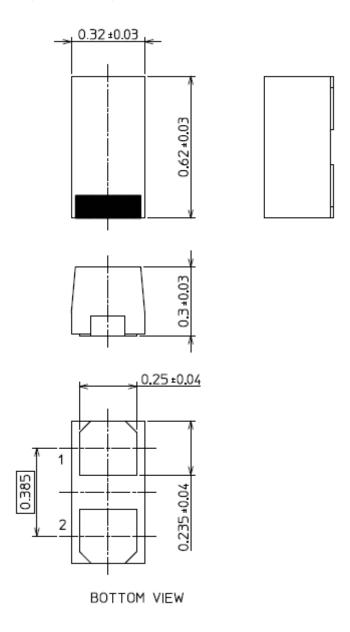


Fig. IEC61000-4-2 (Contact)

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Package Dimensions (Unit : mm)



Weight: 0.2 mg (typ.)

Package Name(s)		
TOSHIBA:		
Nickname:	SL2	

2015-09-29

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